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MULTI-LAYERED SUBSTRATE WITH A BUILT-IN CAPACITOR DESIGN AND A METHOD OF MAKING THE SAME

ABSTRACT OF THE INVENTION

A multi-layered substrate having built-in capacitors is disclosed. The substrate comprises at least one high permittivity of dielectric material filled in the through holes between the power plane and the ground plane so as to form capacitors. The built in capacitors are to decouple high frequency noise due to the voltage fluctuation.